

FIG. 1

FIG. 2 is a schematic diagram of a device 200.

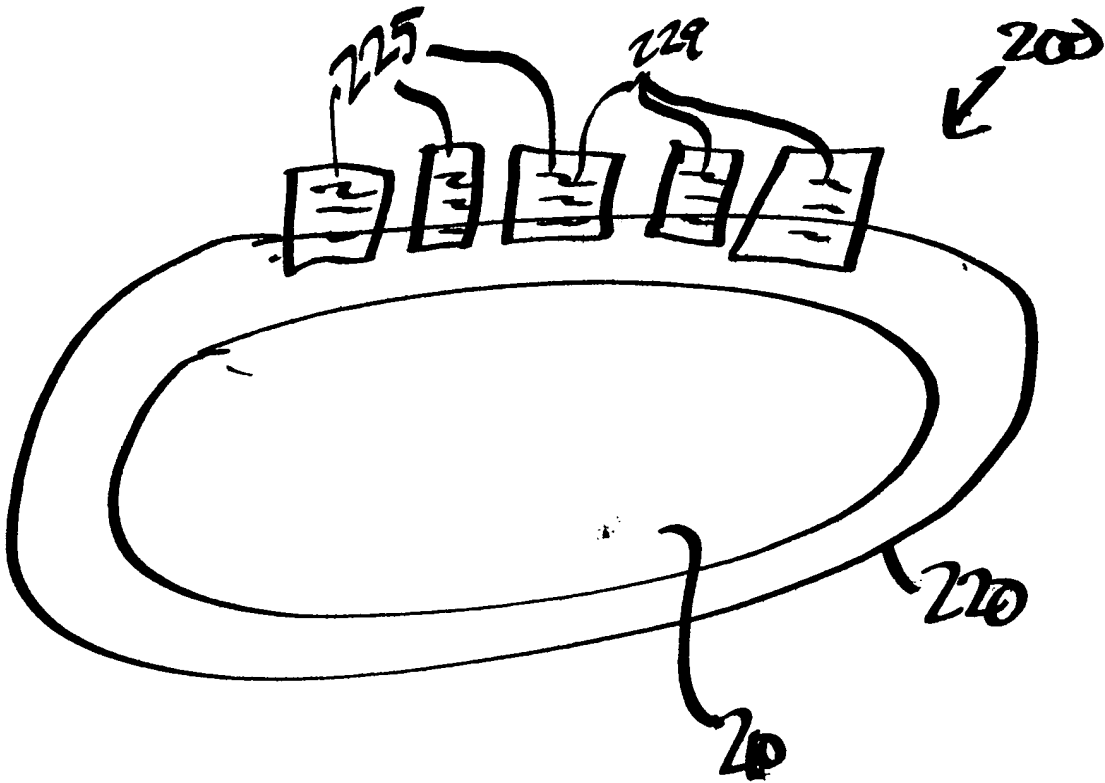


FIG. 2

FIG. 3

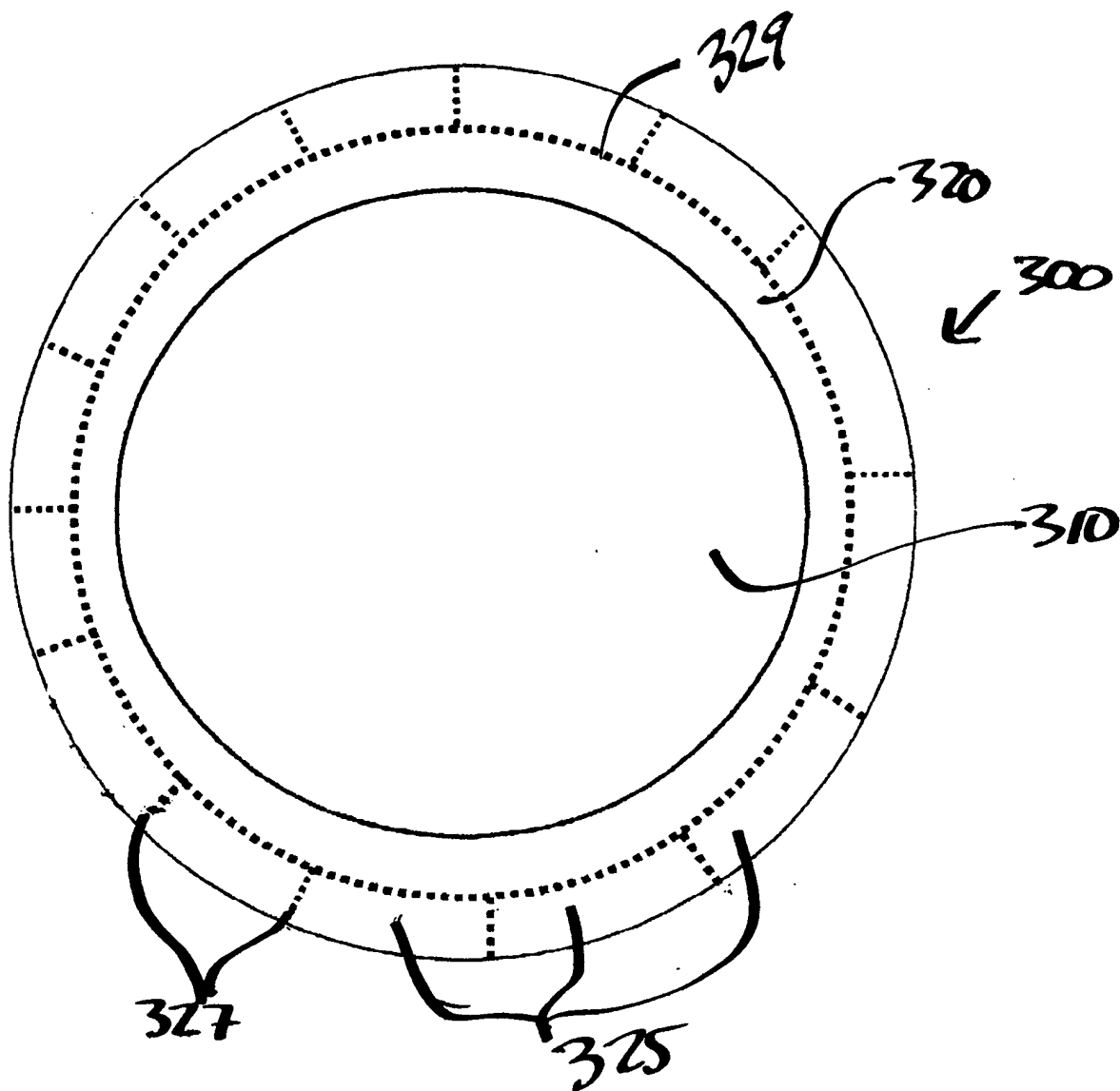


FIG. 3

FIG. 4

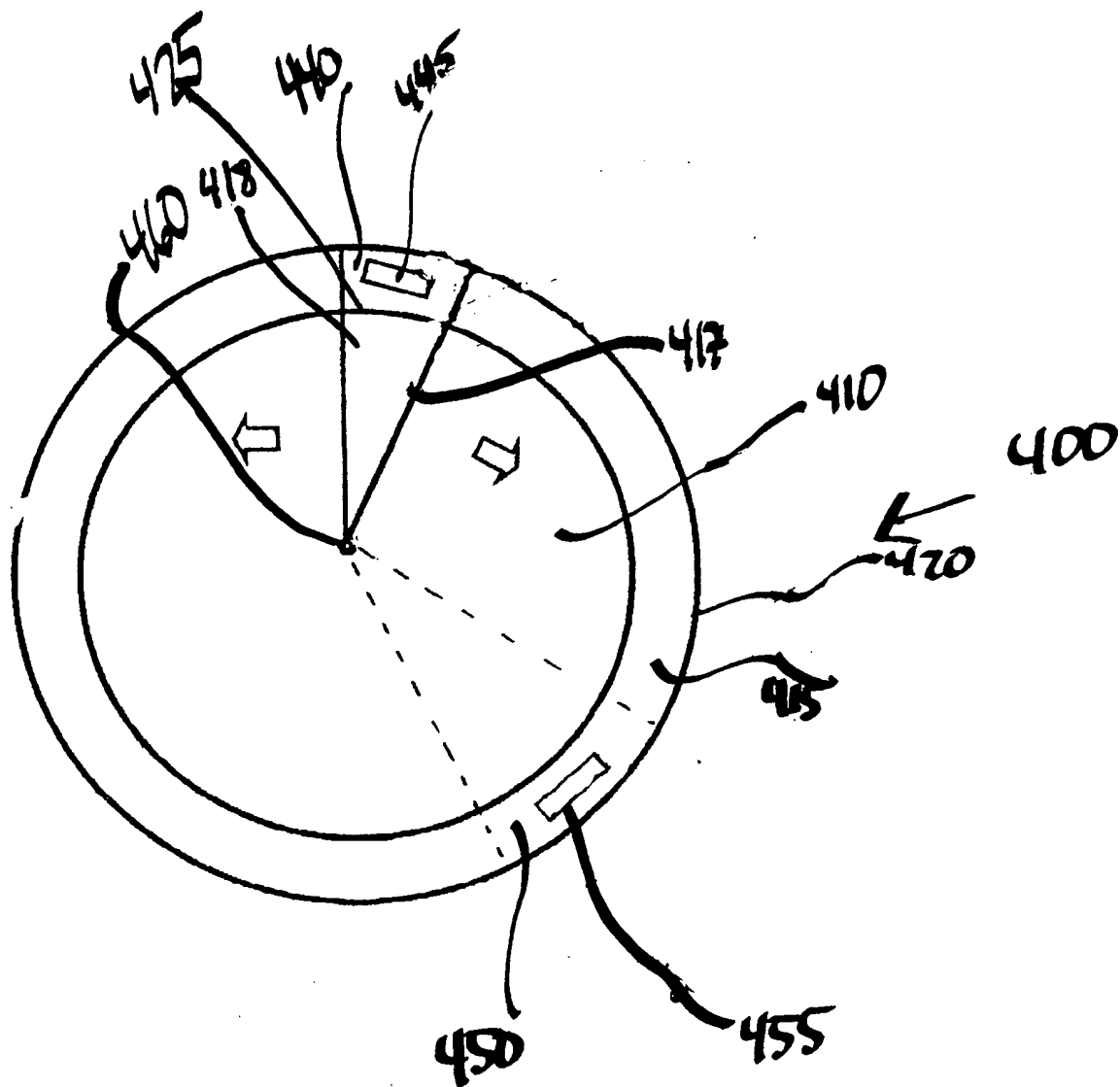


FIG. 4

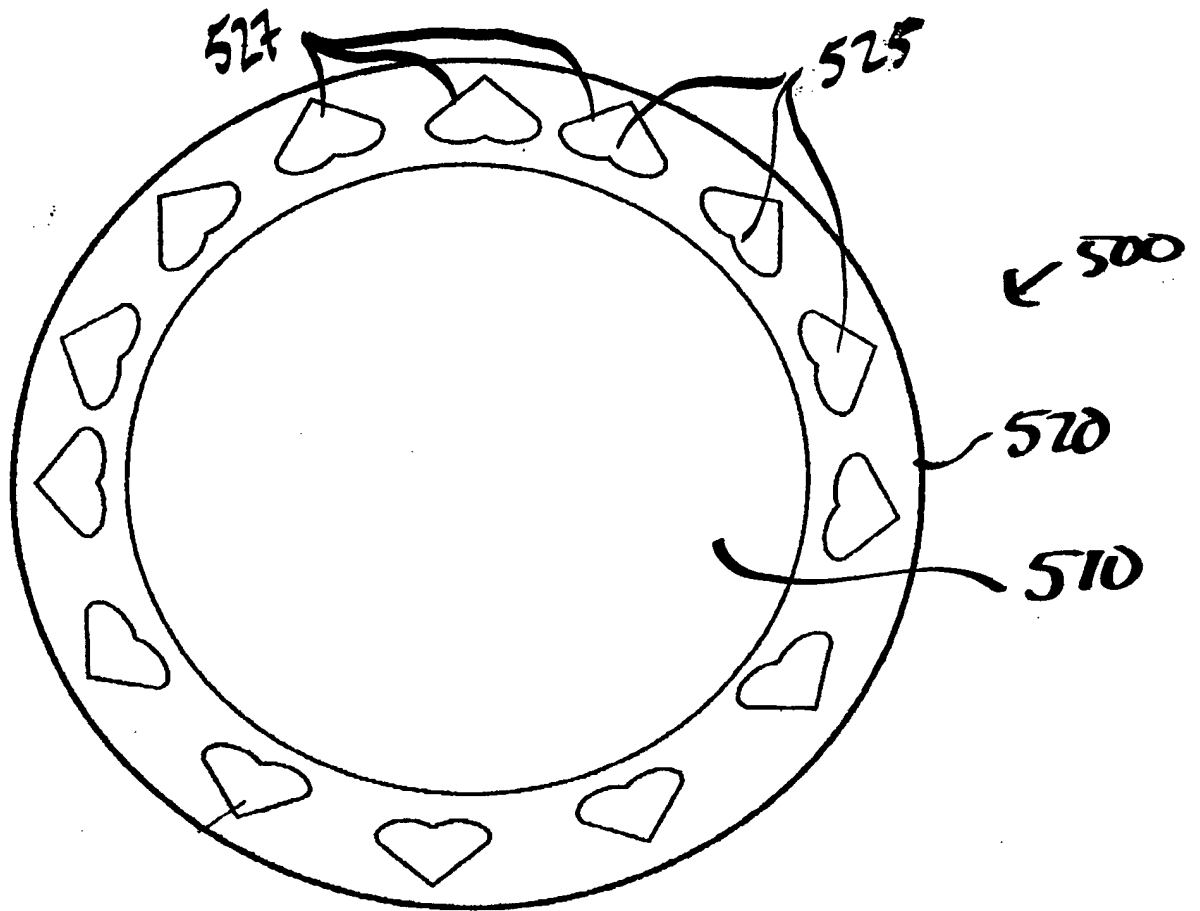
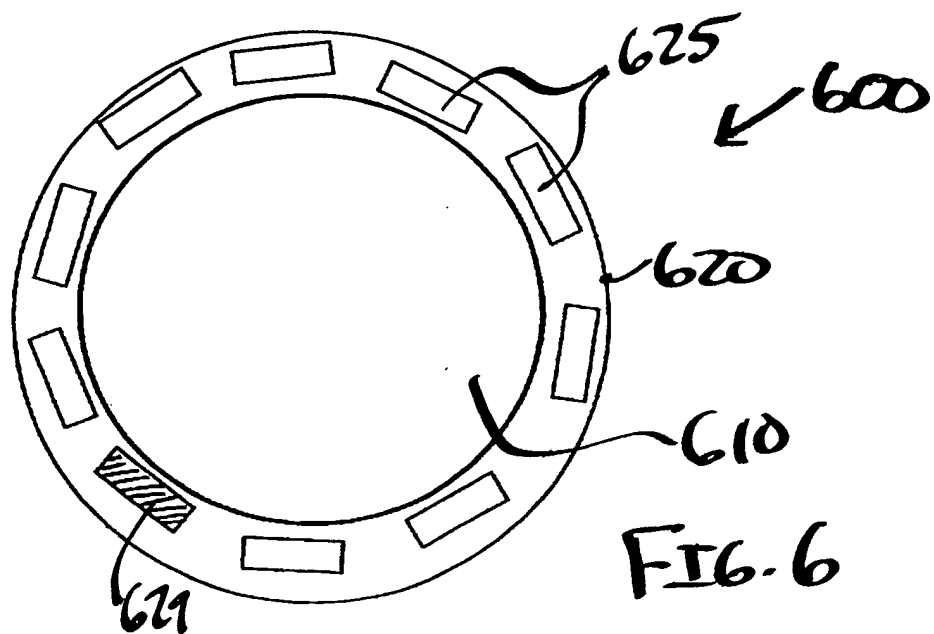


FIG. 5





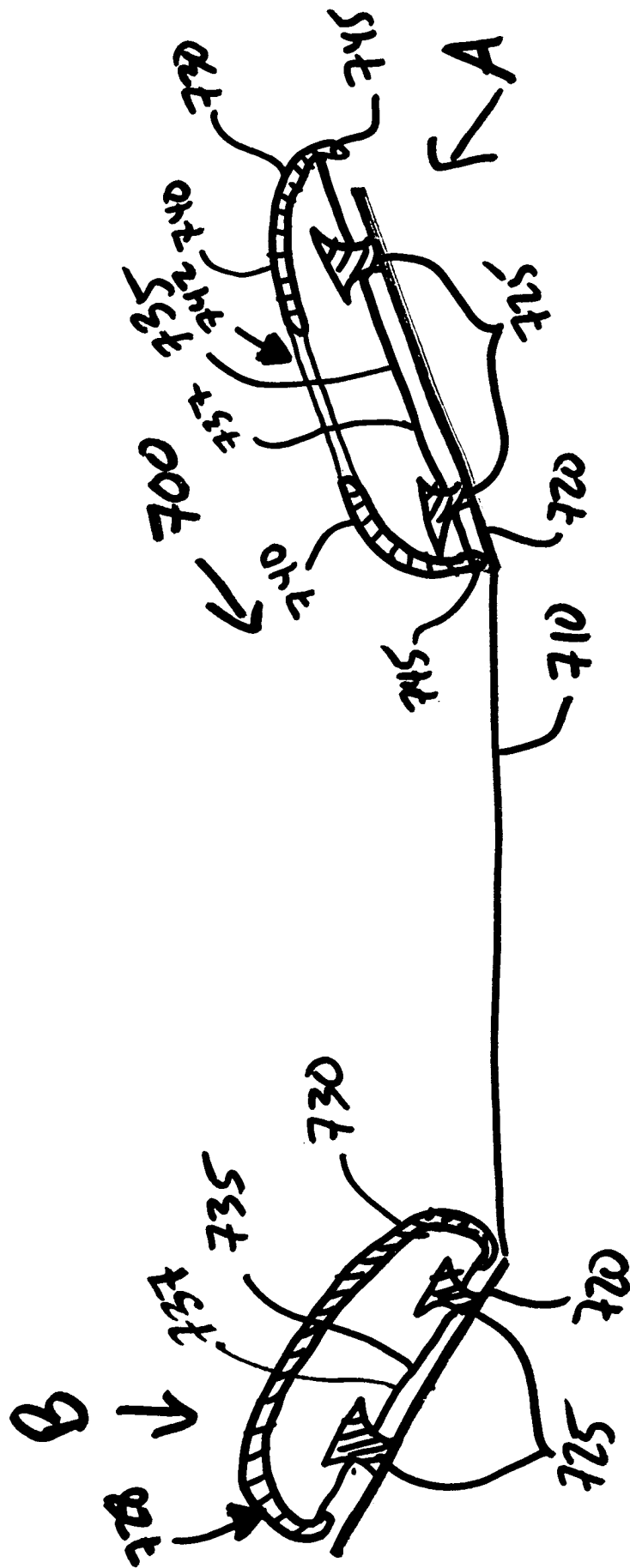


FIG. 8



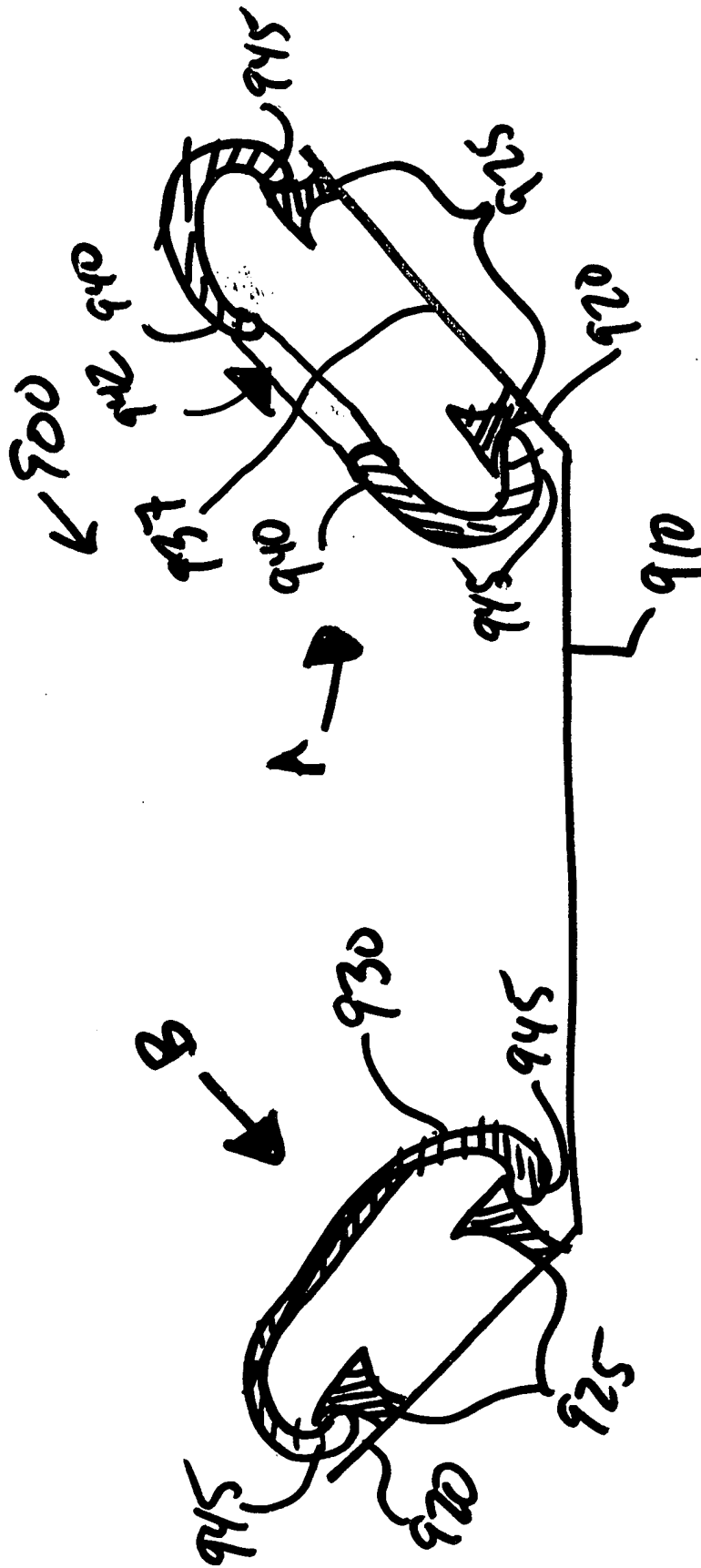


Fig. 9A



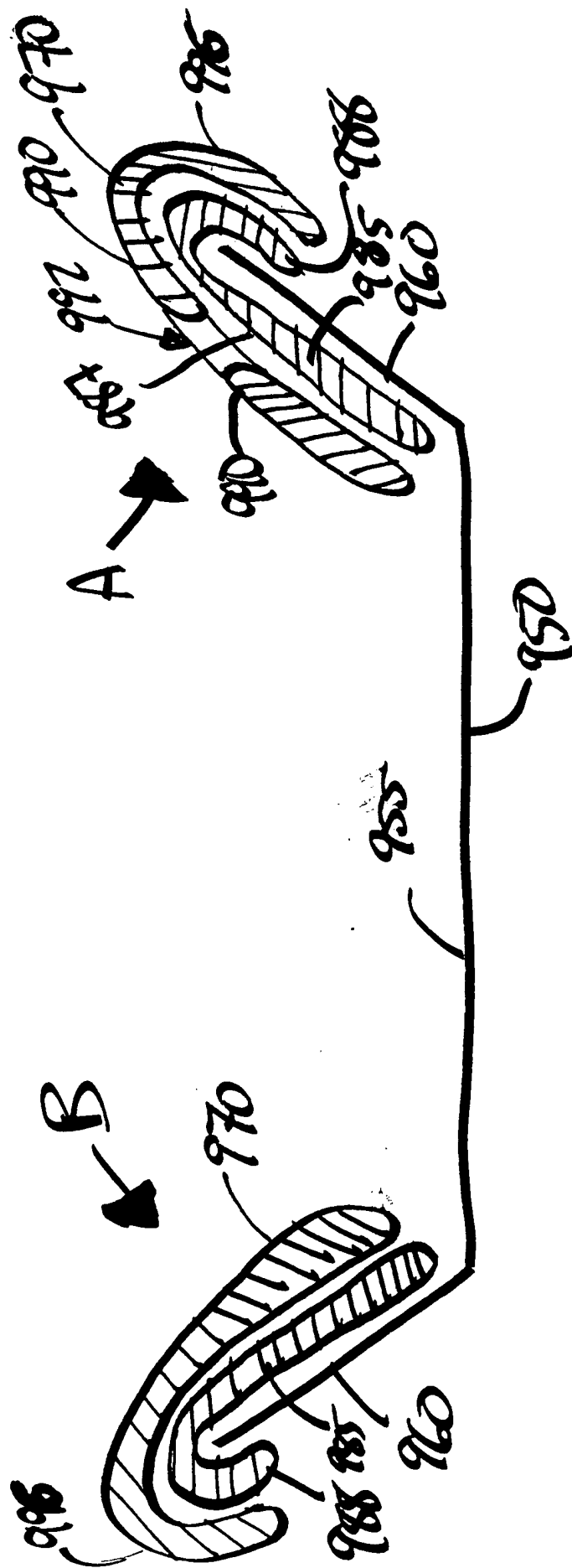


FIG. 9C

FIG. 10G

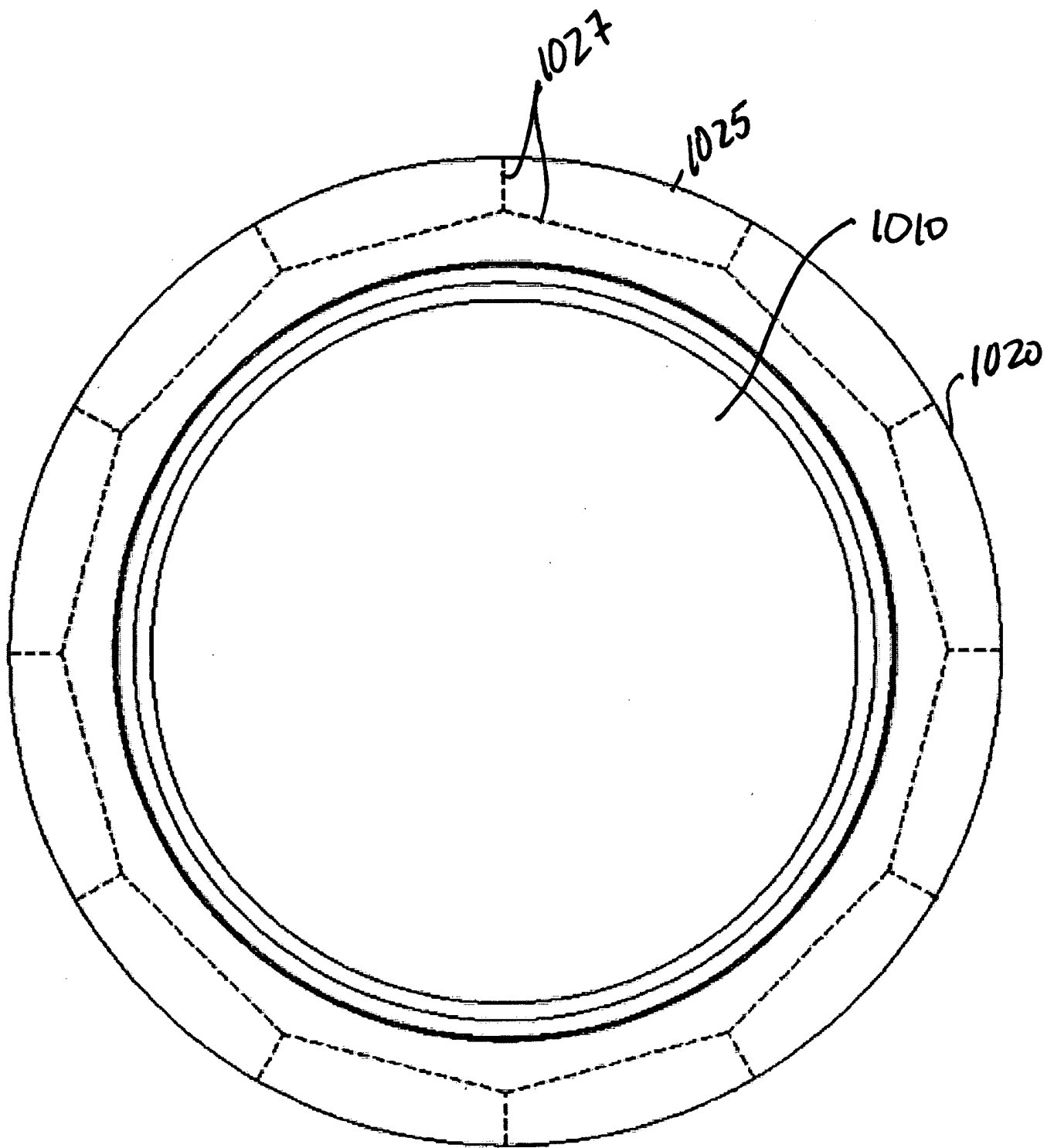


FIG. 10G

FIG. 10A

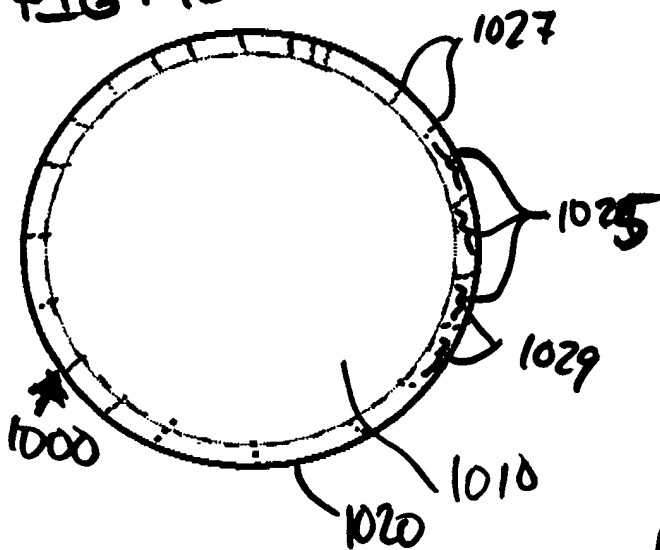


FIG. 10B

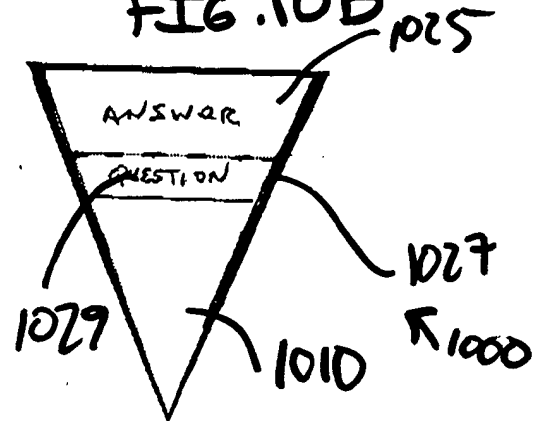


FIG. 10C

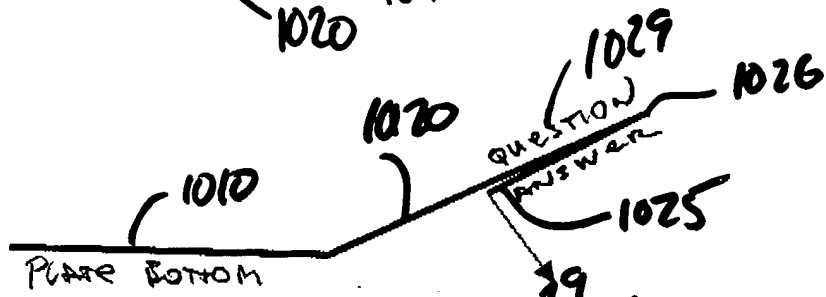


FIG. 10D

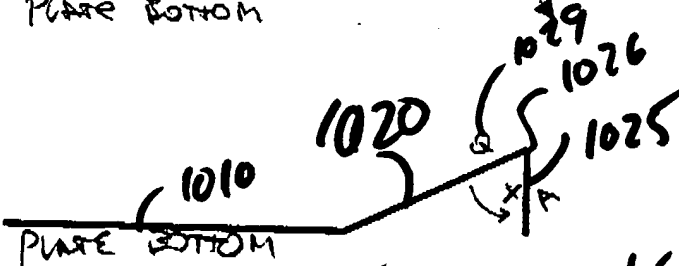


FIG. 10E

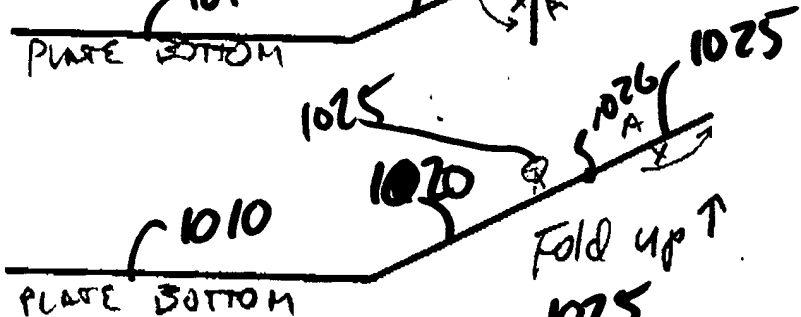
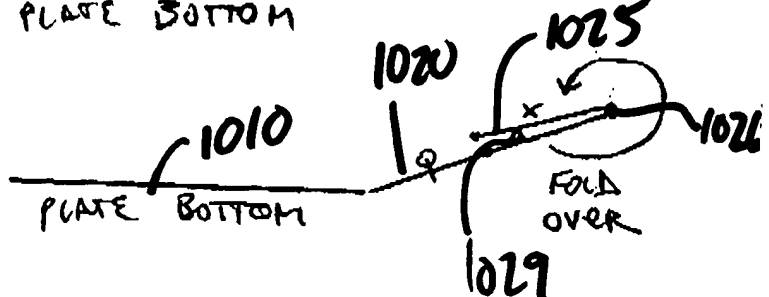


FIG. 10F



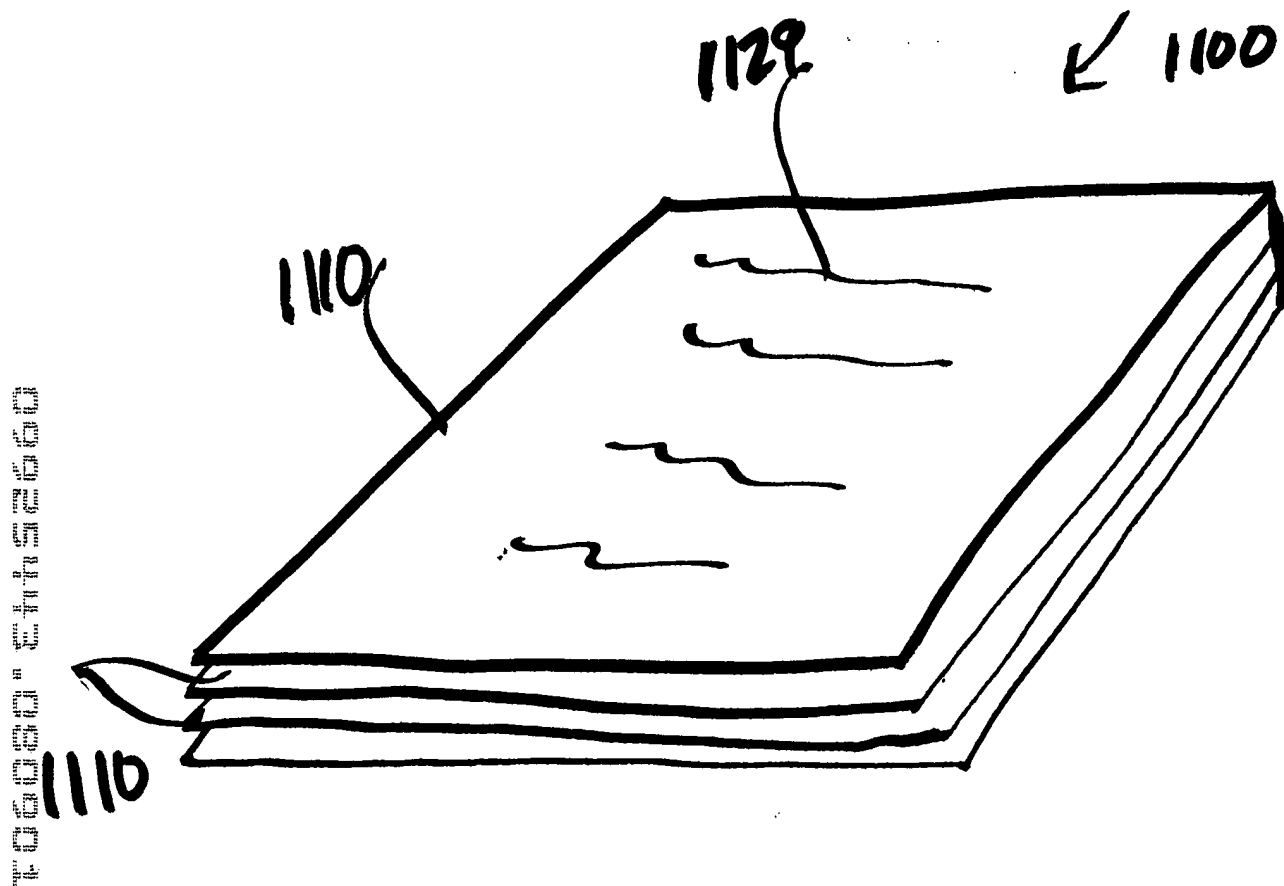


FIG. 11

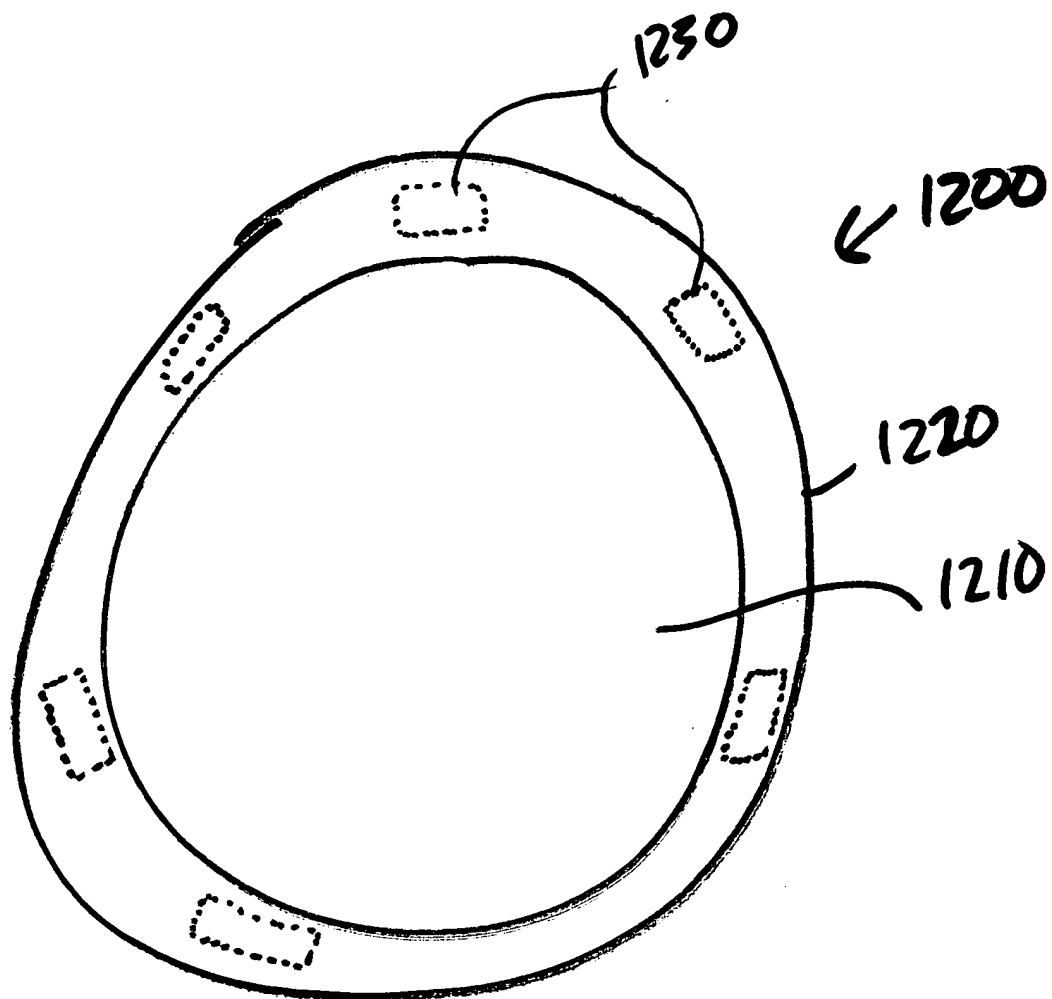


FIG. 12

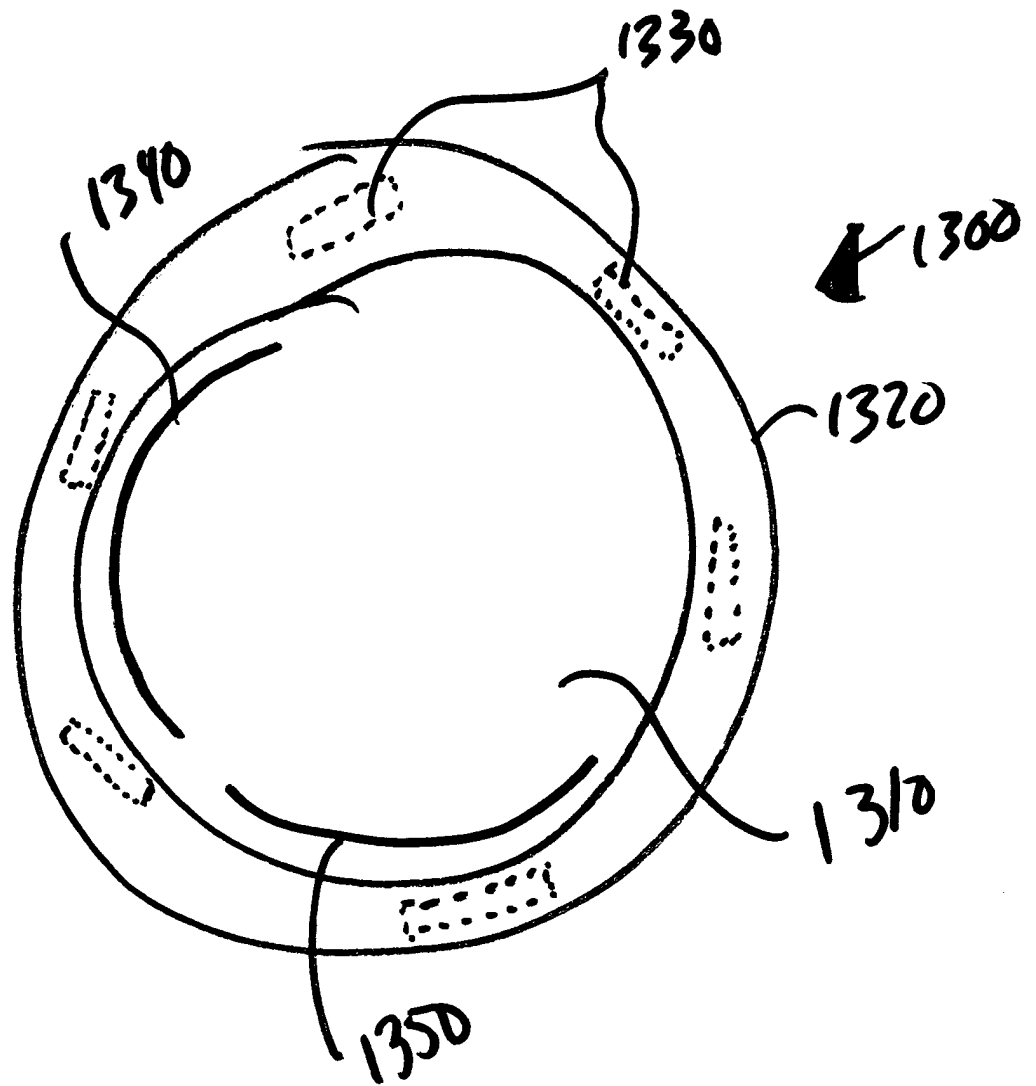


FIG. 13



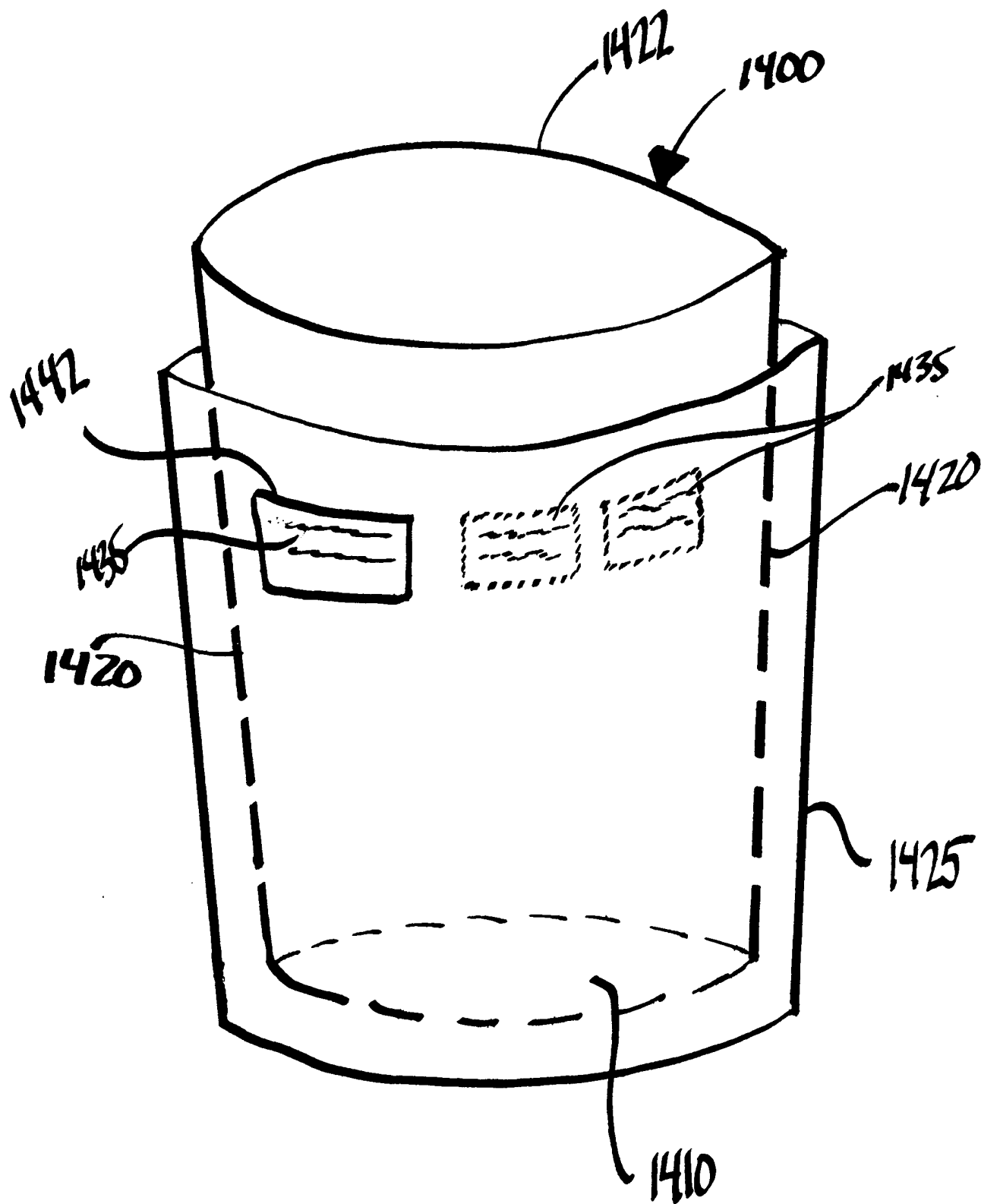


FIG. 14

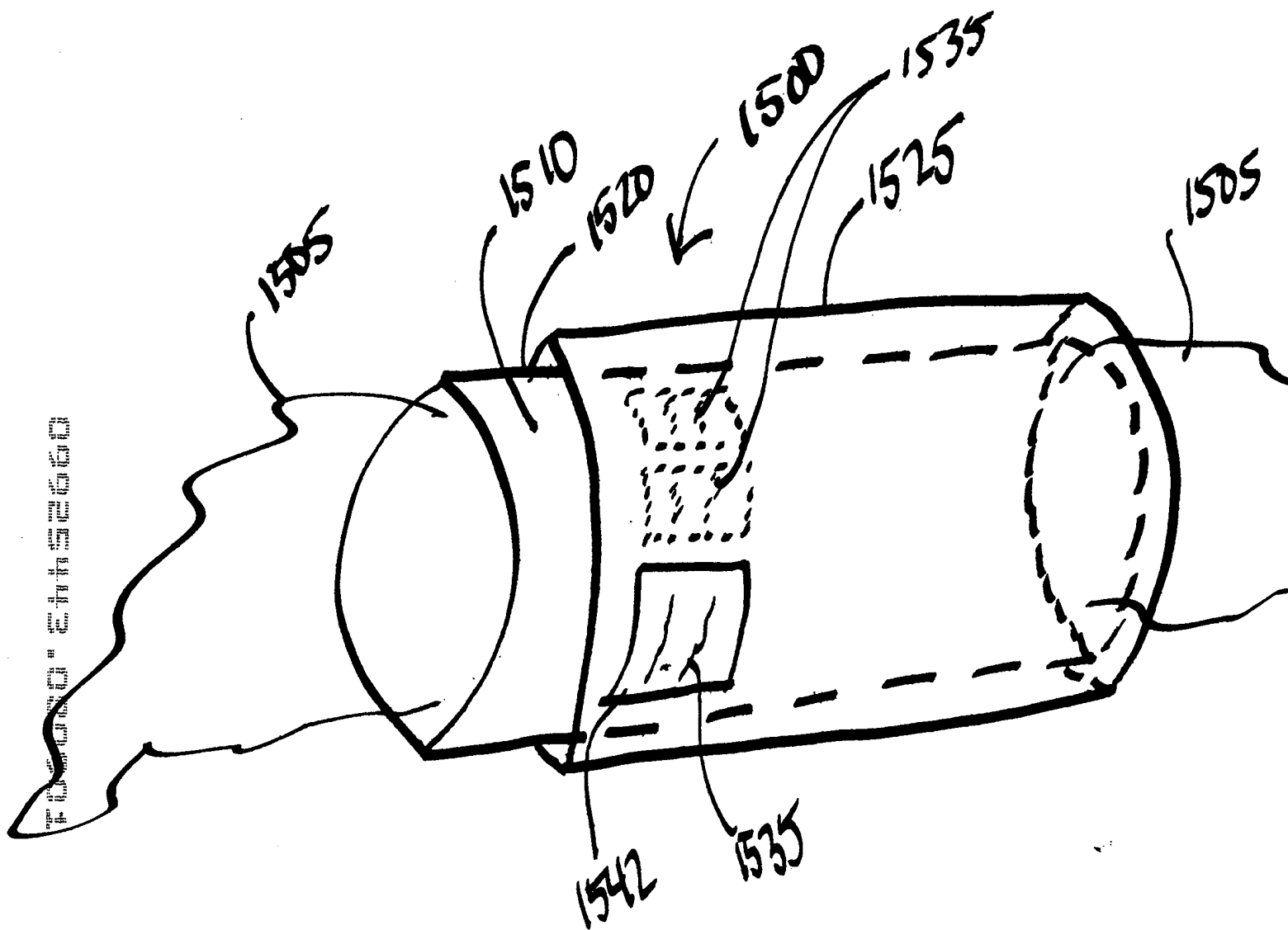


FIG. 15

FIG 16



FIG. 17

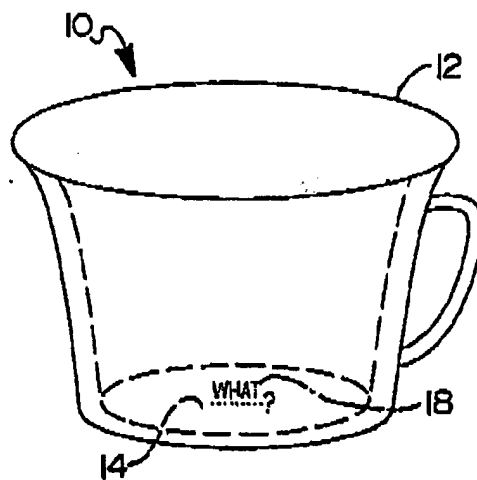
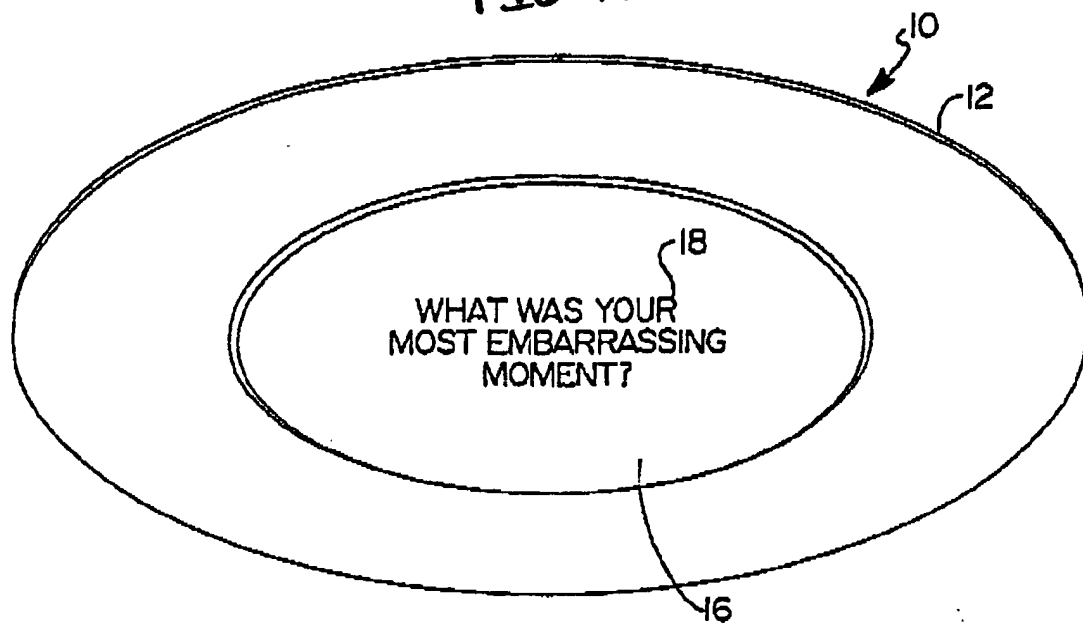


FIG. 18

FIG. 19

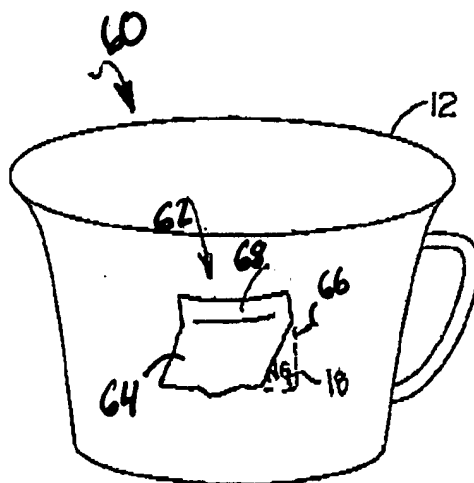
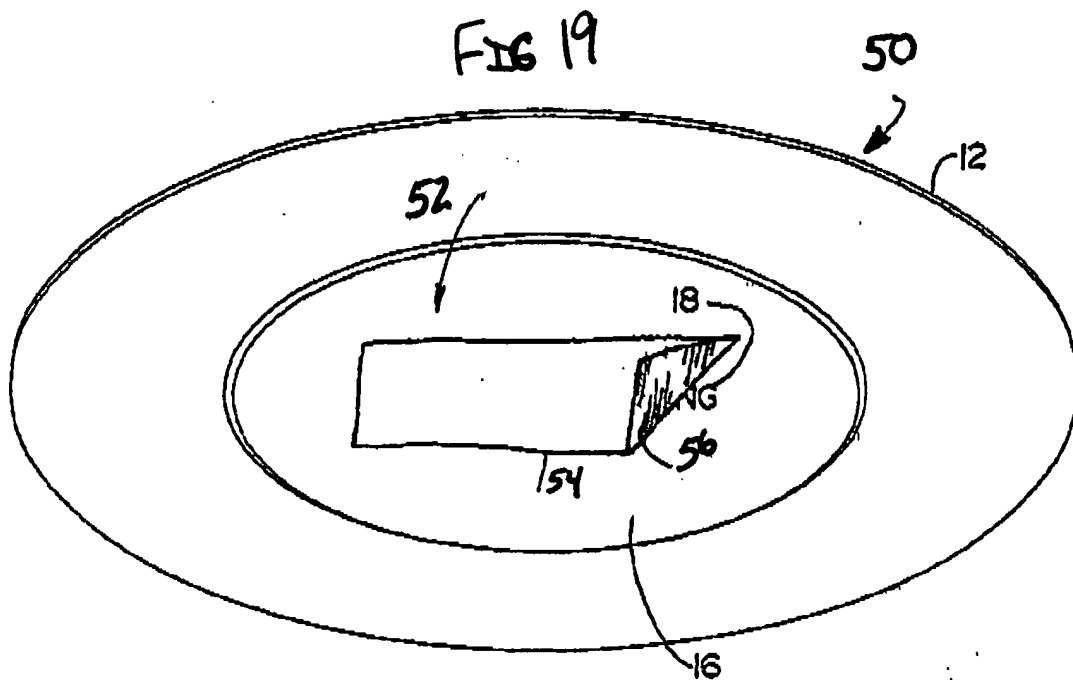


FIG. 20

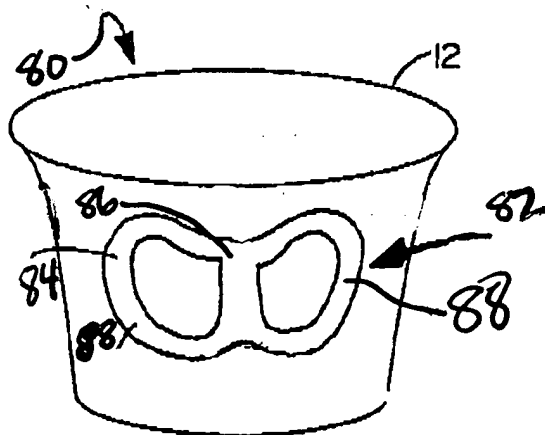
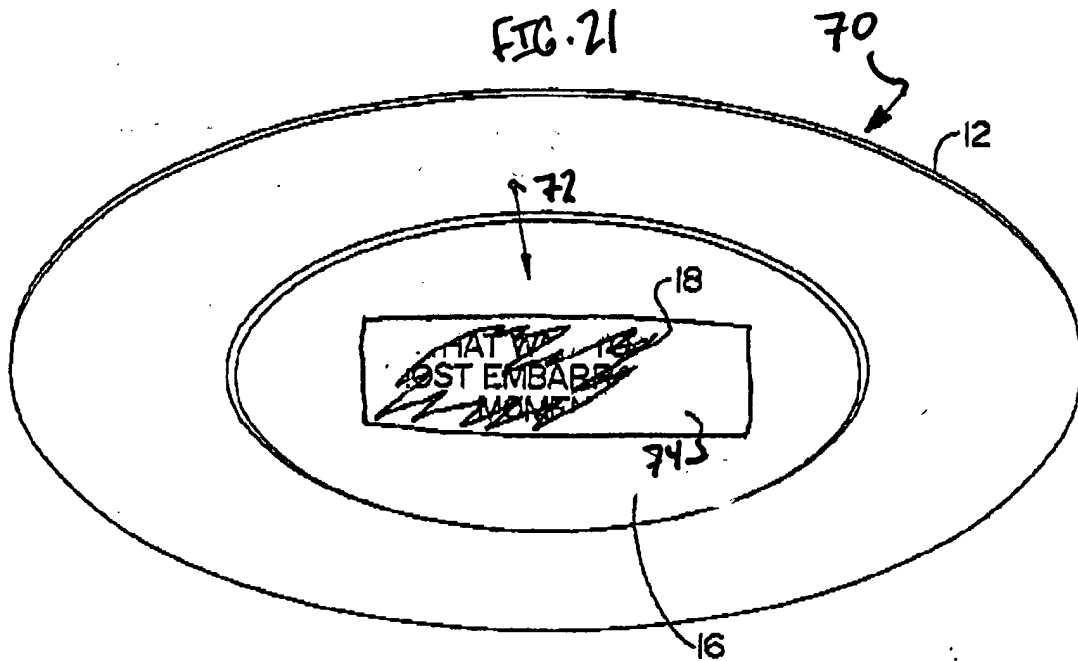


FIG. 22